



IEEE STUDENT BRANCH

























BRANCH ACTIVITIES REPORT 2024 - 25

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27-06-2024

Guest Talk on Persistency and Placement

by Mr. Mr. Mitta Ajay Kumar Reddy, Software Engineer, HCL Tech., Chennai







IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Organizes a Guest talk

FUTURE OF SEMICONDUCTOR



Ar.Mitta Ajay Kumar Redd Software Engineer, HCL Tech., Chennai

27.06.2024

VENUE: TECHTRONICS HALL (10AM TO 11AM

Register @ https://bit.ly/3zkJgxD

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Professor, Dept. of CSE IEEE SB Counsellor

> Co-Convener Ms. Shamini G I

Asst. Prof., Dept. of ECE

Student Organizer Ms. Sapna Bhardwaj

Chair, IEEE WIE AG



Guest talk on Persistency and Placement was organised by IEEE - Women in engineering Affinity Group on 27-06-2024 from 10 to 11 am in the department of ECE. Mr. Mitta Ajay Kumar Reddy, a seasoned software engineer at HCL Technologies, where he is currently contributing to a significant networking project for the esteemed client, Cisco. Mr.Ajay focussed the talk on the expectations of service based industries and product based industries. He pointed out the need such as Skill development, Networking, Resume building, Interview preparation for placement. Also, Mr.Ajay conducted a sample group discussion to showcase how to address the challenge. Student participants raised their queries and got clarified.



27-06-2024

Special Lecture Series on Signal Processing

Topic 1: Signal Processor Teaches Generative AI by Ms. Yuvashree, Chair, IEEE SBC-SPS Topic 2: RF Signal Processing Security and Exploited Vulnerability by Mr. Jayganesh, Member, IEEE SBC-SPS







IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ORGANIZES

SPECIAL LECTURE SERIES ON

SIGNAL PROCESSING

27.06.2024



Ms. Yuvashree M Chair, IEEE SBC - SPS Topic: Signal Processor Teaches Generative AI

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Vice President

VENUE: BOARD ROOM

11.15 AM - 12.15 PM



Mr. Jaiganesh V
Member, IEEE SBC - SPS
Topic: R F Signal Processing Security and
Exploited Vulnerability

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Prof. Dept. of CSE.
IEEE SB Counsellor
Co-Convener
Dr. Mathan N,
Asst. Prof. Dept. of ECE
Advisor, IEEE SBC - SPS
Student Organizers
Ms. Divyasri S,
Vice Chair, IEEE SBC - SPS
Ms. J Charvy Amrisha,
Member, IEEE SBC - SPS

IEEE STUDENT BRANCH - IEEE Signal Processing Society organised special lecture series on Signal Processing that delved into contemporary issues and advancements in signal processing. The lecture series were about the recent trends from signal processing magazine. Lecture began with a presentation by Ms. Yuvashree, Chair, IEEE SBC-SPS, who discussed "Signal Processor Teaches Generative AI." Following Ms. Yuvashree, Mr. Jayganesh, Member, IEEE SBC-SPS presented on "RF Signal Processing Security and Exploited Vulnerability."

Ms. Yuvashree's highlighted the pivotal role of signal processors in enhancing generative AI models. She elaborated on how integrating signal processing techniques can optimize data pre-processing, thereby improving the performance and efficiency of AI applications in areas such as image and speech generation. She also provided case studies to demonstrate the successful implementation of these integrations, showcasing real-world applications and benefits.



Mr.Jayganesh focused on the security challenges inherent in RF (Radio Frequency) signal processing and detailed the various vulnerabilities present in current RF systems and discussed potential exploitation methods that could compromise system integrity.



He emphasized the importance of understanding these security threats and proposed strategies to mitigate them, thereby enhancing the overall security of RF signal processing systems. The seminar provided a comprehensive overview of the intersection of signal processing with generative AI and the critical security considerations in RF signal processing.

03-07-2024

Special Lecture Series on Signal Processing

Topic 1: Transforming ASL to Text by Ms Divyashri, Vice Chair, IEEE SBC-SPS

Topic 2: Hypercpmplex signal and Image Processing by Ms Chethana P, Member, IEEE SBC-

SPS







IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ORGANIZES

SPECIAL LECTURE SERIES ON

SIGNAL PROCESSING

03.07.2024



Ms. Divyasri S, Vice Chair, IEEE SBC - SPS Topic: Transforming ASL To Text: Empowering The Deaf Community

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VENUE:
CENTRE OF EXCELLENCE ON
EMBEDDED AND IMAGING,
ECE GROUND FLOOR

11.15 AM - 12.15 PM

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Dr. E Logashanmugam,
Pro Vice Chancellor
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Dr. G Sundari, Director-Administration
Dr. N M Nandhitha, Dean School of EE
Dr. T Ravi, Head. Dept. of ECE



Ms. Chethana P
Member, IEEE SBC - SPS
Topic: Hypercomplex Signal and
Image Processing

Convener
Dr. M S Godwin Premi,
Prof. Dept. of CSE,
IEEE SB Counsellor
Co-Convener
Dr. Mathan N,
Asst. Prof., Dept. of ECE
Advisor, IEEE SBC - SPS
Student Organizers
Ms. Darrshini.V.B,
Secretary, IEEE SBC - SPS
Ms. K.Poojasri,
Treasurer, IEEE SBC - SPS

IEEE Student Branch - Signal Processing Society organised Special Lecture Series on Signal Processing 03.07.2024 offered a fascinating glimpse into two cutting-edge areas: ASL to text translation and hypercomplex signal processing. Ms.Divyashri.S, Vice Chair, IEEE SBC-SPS presented on developing robust tools to translate American Sign Language (ASL) into text.

Ms.Divyashri.S, delivered the talk on tools to translate American Sign Language (ASL) into text for focusing the Engineering the Deaf community. Also, she focused on leveraging signal processing techniques like image recognition, computer vision, and machine learning. This could revolutionize communication accessibility for deaf and hearing-impaired individuals. The student participants were able to know the available tools and the analytics in that through this special lecture.



On the other hand, Ms Chethana, Member, IEEE SBC-SPS delved into the realm of hypercomplex signal processing. This presentation explored the use of advanced mathematical structures known as hypercomplex numbers to analyze signals and images. Potential applications include the development of more sophisticated filters for noise reduction, improved medical imaging techniques, and advancements in other signal processing areas. The event potentially highlighted a collaborative or "socialistic" approach to research within the signal processing domain.



04-07-2024

Training Session on IEEE V-Tools

by Ms. Harshita, Test Engineer, Caterpillar, Chennai



INSTITUTE OF SCIENCE AND TECHNOLOGY (DEEMED TO BE UNIVERSITY) ACCREDITED WITH GRADE "A++" BY NAAC



IEEE STUDENT BRANCH

CATEGORY - 1 UNIVERSITY BY UGC





















DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING **ORGANIZES**

Training Session on

IEEE V-TOOLS

(Only for IEEE Student Members)









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Ms. HARSHITHA D TEST ENGINEER, CATTERPILLAR, CHENNAI EX-CHAIR, SATHYABAMA IEEE SB

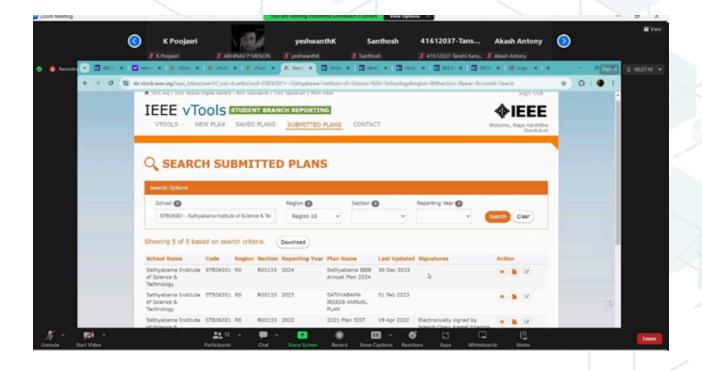
> **Student Organizers** Ms. Dhivya,

Chair, IEEE SB

Ms. Yuvashree M.

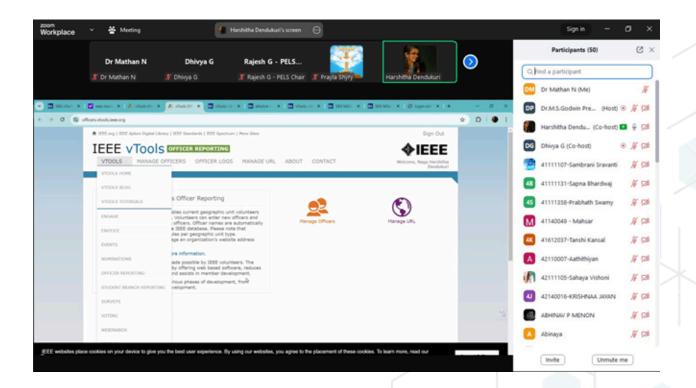
Execom Member, IEEE SB

IEEE Student branch organized technical session on IEEE V-Tools, where Ms. Harshita D delivered her insights on V-Tools for IEEE student members. V-Tools is transforming how IEEE members and volunteers connect, collaborate, and engage within the organization. By streamlining event management and communication, V-Tools facilitates smoother coordination and planning of IEEE events and activities, fostering a sense of community and teamwork. It enables members to efficiently share updates, announcements, and resources, enhancing transparency and accessibility. The feedback mechanisms and reporting features of V-Tools also promote continuous improvement and active participation. Overall, V-Tools is empowering IEEE communities to work more effectively and stay connected, ultimately driving greater engagement and collaboration among members.



Participants learned how to streamline their event organization processes, saving time and reducing errors, while also gaining professional development by enhancing their technical skills. The session emphasized the importance of networking, accessing resources, and customizing tools to meet specific needs, ultimately leading to better coordination and participation in IEEE events. By understanding V-Tools, attendees are now equipped to manage their activities more effectively and gather feedback for continuous improvement, contributing to their personal and professional growth.

Also, the executive members learned about the vtools event reporting for their respective organizational unit.

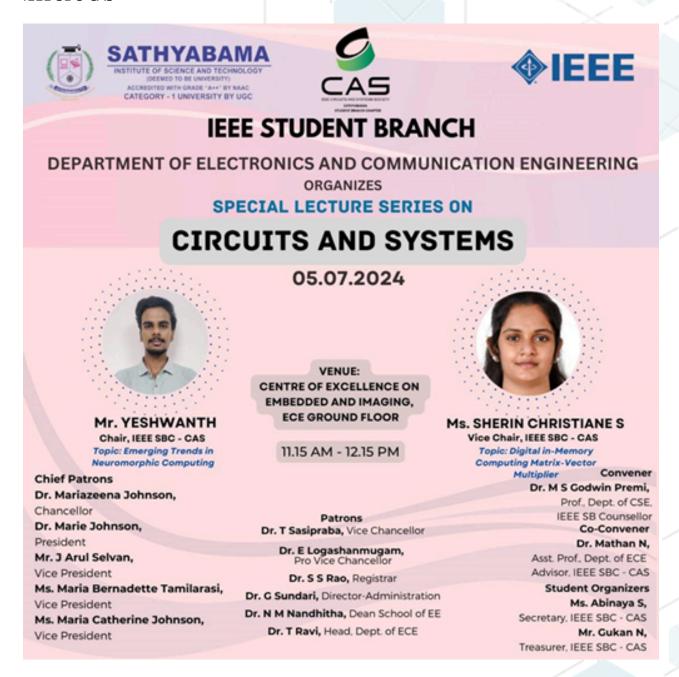


05-07-2024

Special Lecture series on Circuits and Systems

Topic 1: Emerging Trends in Neuromorphic Computing by Mr. Yeshwanth, Chair, IEEE SBC-CAS

Topic 2: Digital In-Memory Computing Matrix Vector Multiplier by Ms. Sherin, Vice Chair, IEEE SBC-CAS



IEEE Student Branch - IEEE Circuits and Systems Society organised a special lecture series on Circuits and Systems by that delved into contemporary issues and advancements in circuits and systems. The lecture series were about the recent trends from Circuits and Systems magazine. The lecture began with a talk by Mr. Yeshwanth K, Chair, IEEE SBC-CAS, who discussed "Emerging Trends in Neuromorphic Computing."

Mr. Yeshwanth's presentation highlighted the pivotal role of neuromorphic computing in advancing AI models. He provided a demonstration of how neural networks work by using the Rosenblatt perceptron, explaining the fundamental principles behind it. He elaborated on how integrating neuromorphic techniques can optimize data processing, thereby improving the performance and efficiency of AI applications in areas such as pattern recognition and sensory processing.



Following Mr. Yeshwanth, Ms. Sherin Christiane S, Vice Chair, IEEE SBC- CAS, presented on "Digital In-Memory Computing Matrix Vector Multiplier." Her presentation focused on the innovative approaches in digital in-memory computing. Sherin detailed the various advancements in matrix vector multiplication and discussed the potential of these methods to enhance computational efficiency. She emphasized the importance of these technologies in modern computing systems and proposed strategies for their implementation, thereby highlighting their impact on the future of digital processing.



08-07-2024

Guest talk on The Wonder Material Graphene: Fabrication of Graphene based Nano Sensors

by IEEE virtual Bureau Speaker, Dr. Sunipa Roy, Secretary IEEE CASS Kolkata section, Associate Professor from Guru Nanak Institute of Technology, Kolkata









IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING **ORGANIZES**

Guest Talk on

THE WONDER MATERIAL GRAPHENE:

Fabrication of Graphene based Nano Sensors



by IEEE Virtual Bureau Speaker



DATE

08 JULY 2024



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IEEE SB Counsellor

Co-Convener Dr. Mathan N,

Asst. Prof., Dept. of ECE Advisor, IEEE SBC - SPS & CAS



Dr. Sunipa Roy

Associate Professor, SMIEEE, Secretary IEEE CASS Kolkata Section, Guru Nanak Institute of Technology, Kolkata

> **Student Organizers** Ms. Yuvashree M, Chair, IEEE SBC - SPS

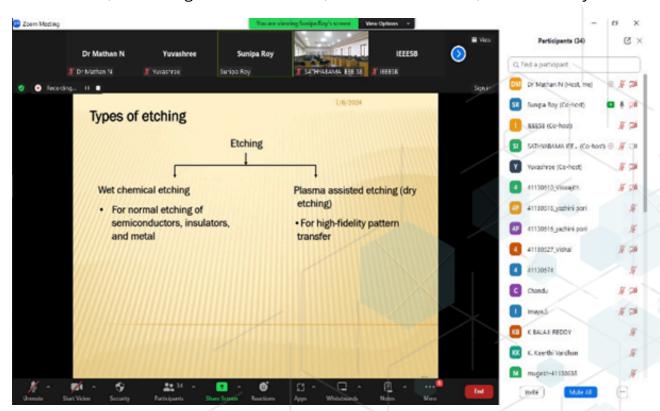
Mr. Yeshwanth K,

Chair, IEEE SBC - CAS

IEEE Student Branch - IEEE Circuits and Systems Society and IEEE Signal Processing Society organized a guest talk on "The Wonder Material Graphene: Fabrication of Graphene-Based Nanosensors by IEEE Virtual Bureau Speaker Dr. Sunipa Roy, Secretary IEEE CASS Kolkata section, Associate Professor from Guru Nanak Institute of Technology, Kolkata in the Department of Electronics and Communication Engineering by blended mode. Dr Sunipa Roy provided an insightful and detailed overview of graphene, highlighting its exceptional properties such as strength, electrical conductivity, and flexibility.



Also, Dr. Roy explained various fabrication techniques, including chemical vapor deposition (CVD), mechanical exfoliation, and chemical reduction, providing participants with a clear understanding of the processes involved in creating high-quality graphene nanosensors. The diverse applications of graphene-based nanosensors in environmental monitoring, medical diagnostics, and industrial process control, showcasing the material's potential to revolutionize these fields were also covered in the presentation. Additionally, Dr.Roy addressed the challenges faced in the mass production and commercialization of graphene-based nanosensors, discussing technical hurdles, cost considerations, and scalability issues.



10-07-2024

IS AI A GAME CHANGER IN THIS ERA?

by Mr. Surya N, Senior staff digital IC verification engineer







IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING ORGANIZES

International Guest Talk on

IS ARTIFICIAL INTELLIGENCE A GAME CHANGER IN THIS ERA?







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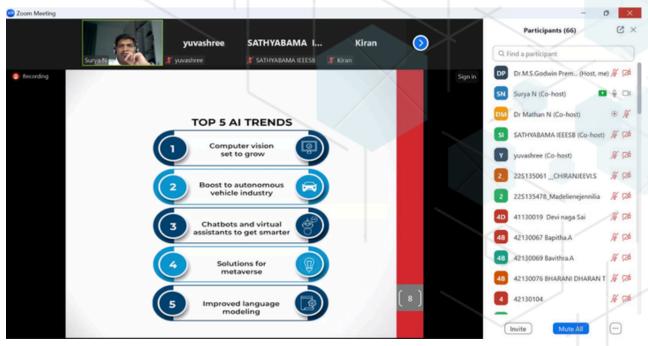
Advisor, IEEE SBC - SPS

Mr. Surya N Senior Staff digital IC Verification Engineer, Maxlinear Asia Singapore Pvt Ltd, Singapore

Student Organizers
Ms. Yuvashree M,
Chair, IEEE SBC - SPS
Ms. M R Sudiksha,
Member, IEEE SBC - SPS

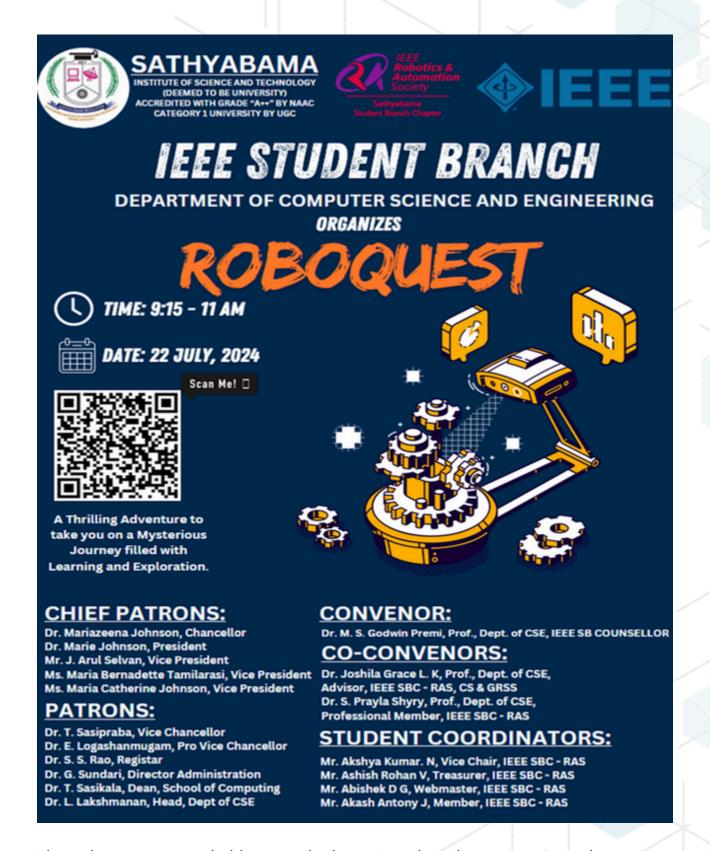
IEEE Student Branch - Signal Processing Society proudly organized an International guest talk on Is AI a game changer in this era?" on 10th July 2024 virtually via zoom video conference. Mr. Surya N, Senior staff digital IC verification engineer, Maxlinear Asia Singapore Pvt Itd as our guest speaker for the session. The event began with a comprehensive overview of AI, detailing its evolution, current capabilities, and significant impact on various sectors such as healthcare, finance, and autonomous systems. The speaker emphasized AI's ability to analyze vast amounts of data, learn from patterns, and make intelligent decisions, revolutionizing how problems are solved and processes are optimized.

The talk highlighted the transformative potential of AI in the VLSI domain, exploring how AI-driven innovations are reshaping the landscape of semiconductor design and development.

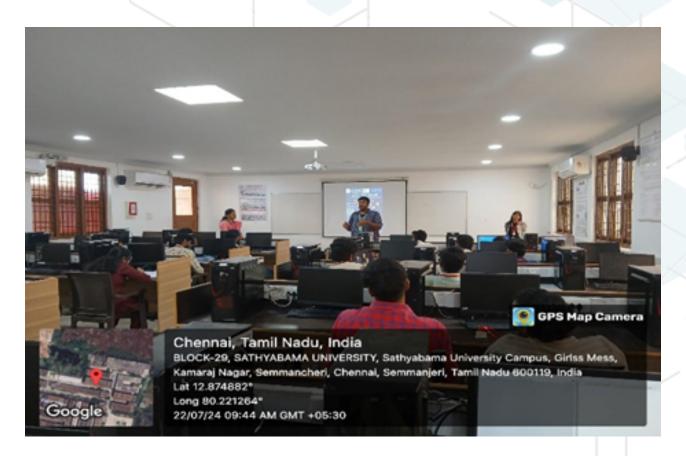


Following this introduction, the talk transitioned to focus on Al-driven innovations in semiconductor design and VLSI technology, showcasing the synergies between Al and VLSI in advancing technology. This intersection is paving the way for more efficient, powerful, and intelligent systems, underscoring Al's critical role in shaping the future of electronics and integrated circuits. The discussion also explored the future scope of VLSI, highlighting advancements in nanotechnology, quantum computing, and Al integration. These advancements are paving the way for more efficient, powerful, and intelligent systems, underscoring Al's critical role in shaping the future of electronics and integrated circuits.

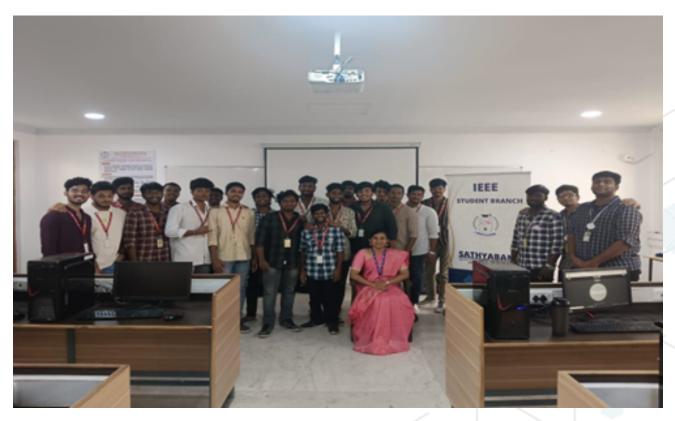




The RoboQuest event, held on 22nd July 2024 at the Edge Computing Lab, was an exciting robotics quiz designed to test participants' knowledge in various aspects of robotics.

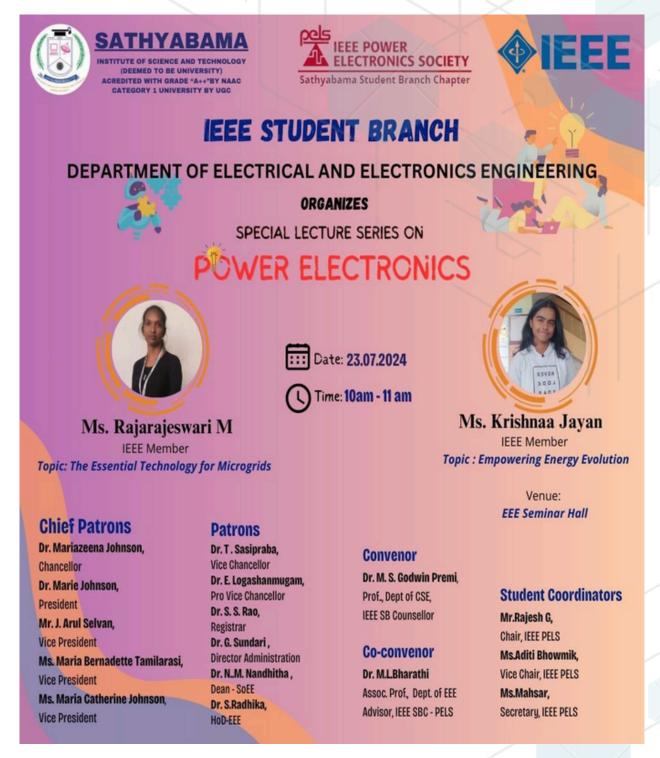


The competition was structured into multiple rounds, each focused on different areas of robotics, including its history, current technologies, programming, and practical applications. The quiz was conducted in a team format, with each team consisting of two members, allowing for collaborative problem-solving and knowledge sharing.



The first round was a Robotics-based Quiz, where participants answered questions related to the field of robotics. The second round, titled Connections, added a fun twist to the event. In this round, teams had to connect various robotics-related concepts, making it both challenging and enjoyable for all participants. The event provided a great opportunity for attendees to showcase their knowledge, engage in friendly competition, and learn more about the rapidly evolving field of robotics. The event concluded with enthusiastic participation and a sense of accomplishment for all involved.

23-07-2024 Special Lecture series on Power Electronics



The "SPECIAL LECTURE SERIES ON POWER ELECTRONICS" was held on 23rd July 2024, from 10:00 AM to 11:00 AM, in the EEE Seminar Hall. This event was organized by the IEEE PELS SOCIETY to discuss the advancements and essential technologies associated with microgrids. The speakers for the seminar were Krishnaa Jayan and Rajarajeshwari.M, both third-year IEEE members..



The seminar was graced by the presence of the Head of the Department (HOD) of EEE , Dr. S. Radhika ma'am and the IEEE SBC Advisor Dr. M. L. Bharathi ma'am. Their attendance underscored the importance of the topics discussed and provided additional insights. The seminar began with a warm welcome address rendered by Sniya, a second-year IEEE member. In her address, Sniya greeted the attendees and introduced the theme of the seminar. She highlighted the importance of microgrids in the modern energy landscape and set the stage for the presentations that followed. Her enthusiastic and articulate speech set a positive tone for the event.





The HOD Dr.S. Radhika ma'am implored that we'll able to explore a lot through IEEE. She congratulated the IEEE leads and board members for organizing the event. And she appreciated the audience for being present for the event. Krishnaa Jayan focused on the theme "Empowering Energy Evolutions." She discussed the evolution of energy systems and the role of microgrids in transforming energy distribution and management. Her presentation covered the benefits of microgrids in enhancing energy efficiency, reliability, and sustainability. She also highlighted various case studies where microgrids have been successfully implemented to provide resilient power solutions.





Rajarajeshwari's theme was "The Essential Technology for Microgrids." She provided an in-depth analysis of the technological components that are critical for the operation and management of microgrids. Her discussion included advancements in energy storage systems, smart grid technologies, and renewable energy integration. She emphasized the importance of adopting cutting-edge technologies to optimize the performance of microgrids and ensure seamless integration with the main power grid. Dr. M. L. Bharathi ma'am congratulated the organizers and speakers for successfully conducting the event. She praised the informative presentations and the smooth execution of the seminar, acknowledging the hard work and dedication of the IEEE student members. Her words of encouragement were well-received and provided a fitting conclusion to the event. The seminar concluded with a vote of thanks delivered by Krishnaa Jayan. She expressed her gratitude to the esteemed faculty members, Dr. S. Radhika ma'am and Dr. M. L. Bharathi ma'am, for their presence and support. She thanked the attendees for their active participation and engaging questions, which contributed to the success of the seminar. Additionally, she acknowledged the efforts of her fellow IEEE members and volunteers who worked tirelessly behind the scenes to ensure the smooth execution of the event. Her heartfelt thanks and closing remarks brought the seminar to a fulfilling end.

The seminar was well-received by the attendees, who appreciated the detailed presentations and the relevance of the topics to current energy challenges.

The event concluded with a vote of thanks to the speakers, attendees, and organizers. Overall, the seminar on microgrids successfully highlighted the critical role of microgrids in the future of energy systems and inspired further interest and research in this field

ANALYSE YOUR VIEW







IEEE STUDENT BRANCH

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING



ANALYZE YOUR VIEW

"REPORTING EARTH'S RESILIENCE AND VULNERABILITIES THROUGH DRONE AND SATELLITE IMAGERY."









https://forms.gle/iQYZhBT2eDa97LY2A

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Dr. L. Lakshmanan, Head, Dept. of CSE

CONVENOR:

Dr. M. S. Godwin Premi.

Prof, Dept of CSE,

IEEE SB Counsellor

Co-CONVENORS:

Dr Joshila Grace L.K,

Prof, Dept of CSE,

Advisor, IEEE SBC -GRSS, RAS & CS

Dr S Prayla Shyry,

Prof, Dept. of CSE

Professional Member, IEEE SBC- GRSS

Event Co-ordinators:

Ms. Veeramaneni Sri Sathvika,

Chair, IEEE SBC-GRSS

Mr.B.S.P.R.Sathyanarayana,

Treasurer, IEEE SBC-GRSS

Mr.Aathithiyan R,

Member, IEEE SBC-GRSS



The competition consists of multiple rounds, each designed to assess different aspects of puzzling skills and challenge participants in unique ways.

In the First Round, titled College Places Puzzle, participants will solve puzzles related to various locations on campus. The objective is to find and identify specific college places based on the given clues, testing participants' knowledge of their surroundings and ability to connect hints to real locations.

The Second Round, Disaster Picture Puzzle, involves each team receiving a picture depicting a disaster. The teams must identify the disaster shown in the image and prepare a detailed report. This report should include the type of disaster, its impact, and any relevant information, combining visual recognition with research and analytical thinking.

Finally, the Third Round is a Quiz that covers various topics related to puzzles and general knowledge. Teams will participate by answering questions quickly and accurately to earn points, challenging their knowledge and speed in a competitive environment.

These rounds together create a dynamic and engaging experience for participants, testing their problem-solving abilities, creativity, and teamwork.

25-07-2024 Special Lecture series on Power Electronics



A special lecture series on power electronics was conducted on 25rd July 2024, from 10:00 AM to 11:00 AM in the EEE Seminar Hall. The event was aimed at exploring the latest developments and critical technologies in the field of microgrids. The seminar featured two main speakers, Sniya.A and Gladish.S both IEEE members from the second year.The session commenced with a welcome address delivered by Haritha.D,second-year IEEE member. Haritha warmly greeted the participants and set the tone for the seminar by emphasizing the importance of microgrids in today's energy landscape. Her introductory remarks laid a solid foundation for the presentations that followed, engaging the audience and setting a positive atmosphere.



Sniya.A, IEEE member from 2nd year presented on "Education in electrification for societal sustainability", discussing the transformation of energy systems and the pivotal role of

microgrids. She highlighted how microgrids contribute to energy efficiency, reliability, and sustainability. Sniya also presented various case studies demonstrating successful microgrid implementations and their impact on providing resilient power solutions.

Gladish.S, IEEE member from 2nd year addressed "Trends in DC microgrids", offering an in- depth overview of the key technological components crucial for microgrid operation and management. She covered advancements in energy storage, smart grid technologies, and the integration of renewable energy. Gladish stressed the necessity of leveraging advanced technologies to enhance microgrid performance and ensure smooth integration with the main power grid.

The seminar was honored by the presence of IEEE SBC Advisor Dr.M.L.Bharathi ma'am. Bharathi ma'amimplored that we'll be able to explore lot through IEEE. She congratulated the IEEE leads

and boardmembers for organizing the event. And she appreciated the audience for being presentfor the event. She also congratulated the organizing team and the speakers for the successful execution of the event. She praised the insightful presentations and the well-coordinated organization of the

seminar, acknowledging the hard work and dedication of the IEEE student members. Her positive remarks were a fitting end to the event.

SPEED NETWORKING



The Speed Networking event, held on 25th July 2024 at Room No - 440, St. Paul's Block, was designed to encourage dynamic networking and knowledge sharing. With 60 attendees, the event was structured into multiple rounds to facilitate fast-paced interactions. The first round was a tech-related quiz, where participants tested their knowledge in various areas of technology.

In the second and third rounds, participants were divided into small groups.



Each group was assigned a topic related to technology or the latest trends, and within a specified time limit, the members of each group discussed the topic, shared their insights, and collaborated to explore relevant subjects. This format allowed for meaningful connections and discussions on pressing technological issues, offering participants the opportunity to learn, share, and network in an interactive, engaging setting. The event fostered connections, enhanced knowledge exchange, and provided a platform for participants to engage with others in the field of technology.



26-07-2024 IEEE WIE AG & TECHNICAL SOCIETIES INAUGURATION



The inauguration ceremony commenced with a warm welcome address by Dr. M.S. Godwin Premi, highlighting the significance of IEEE in empowering students and professionals in technical fields. The newly formed affinity group and student branch chapters aim to foster innovation, research, and knowledge-sharing among budding engineers and researchers at Sathyabama Institute of Science and Technology. Mr. J. Arul Selvan, Vice President, Sathyabama Institute of Science and Technology in his inaugural address, emphasized the role of IEEE in shaping the future of engineering and technology through collaborative learning and global exposure. He congratulated the institute for taking the initiative to expand its IEEE presence by forming multiple societies and the Women in Engineering (WIE) Affinity Group.

Dr. T. Sree Sharmila, Chair of IEEE WIE Madras Section, elaborated on the importance of women empowerment in STEM and how the affinity group will inspire and encourage more female students to excel in engineering and leadership roles.



She encouraged active participation in events, conferences, and workshops hosted by IEEE societies. Vice Chancellor Dr. T. Saipraba expressed her appreciation for the efforts of the student branch and faculty coordinators in establishing these societies. She highlighted the need for multidisciplinary collaborations to address real-world challenges and urged students to leverage IEEE's resources effectively. Director of Administration, Dr. G. Sundari, applauded the collective efforts and stressed the importance of ethics and sustainability in technological advancements. She also acknowledged the role of IEEE in bridging academia and industry.

The approvals for all societies were ceremonially handed over by Dr. T. Sree Sharmila to the respective chairs, marking a new chapter for Sathyabama's IEEE student community. Each chair expressed their gratitude and shared their vision for their respective societies, setting ambitious goals for the upcoming academic year. Students were enthusiastic about the opportunities these societies would bring, such as international collaboration, exposure to cutting-edge technologies, and networking with experts. The event concluded with a vote of thanks by Ms. Dhivya, Chair of IEEE Student Branch, who recognized the support of the management, faculty, and IEEE members in making this event a grand success.

The ceremony was followed by a networking session where students, faculty, and IEEE representatives discussed plans for future activities, including technical symposiums, coding competitions, and guest lectures. The occasion also served as an inspiration for students to actively contribute to IEEE's mission and engage in community-building initiatives. This momentous event has laid a strong foundation for a vibrant IEEE community at Sathyabama, aiming to nurture innovation, diversity, and excellence in engineering and technology.

23-08-2024

International guest talk: "Exploring Synergies: Space Science and Electronics"

by Ms.Vaishali, Post Graduate Research Assistant, University of Glasgow, United Kingdom







IEEE STUDENT BRANCH

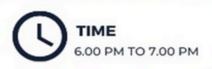
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING **ORGANIZES**

COMMEMORATING NATIONAL SPACE DAY 2024

Guest Talk on

Exploring Synergies: Space Science and Electronics







Johnson.

Vice President

Ms. Maria Catherine Convener Dr. M S Godwin Premi, Prof., Dept. of CSE

IEEE SB Counsellor

high radiation levels and temperature extremes.

Dr. T Sasipraba, Vice Chancellor Dr. E Logashanmugam, Pro Vice Chancellor

Patrons

Dr. S S Rao, Registrar

Dr. G Sundari, Director-Admin Dr N M Nandhitha,

Dean, School of EE Dr T Ravi,

Head, Dept. of ECE

Ms. Vaishali M Post Graduate Research Assistant, University of Glasgow, **United Kingdom** Student Organizers

Ms. Sherin Christiane S, Vice Chair, IEEE SBC - CAS Ms. Dhamini M J.

Webmaster, IEEE SBC - CAS

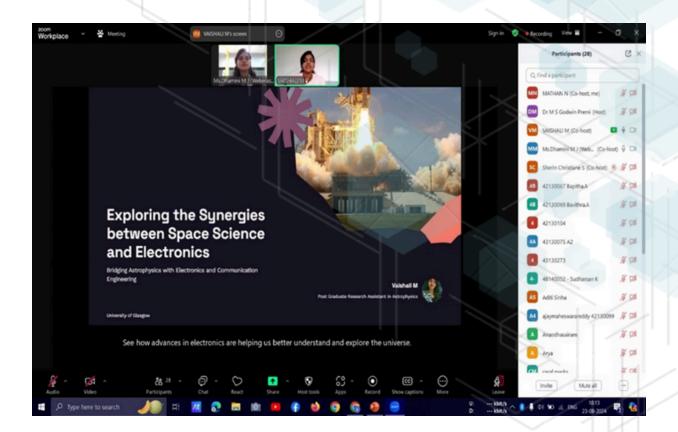
IEEE Student Branch Chapter IEEE Circuits and Systems Society organized an International guest talk Commemorating National Space Day 2024 on Exploring Synergies: Space Science and Electronics, hosted by Ms. Vaishali, Post Graduate Research Assistant, University of Glasgow, United Kingdom on 23rd August 2024 virtually via zoom meeting. Ms. Vaishali Muthuraman as our guest speaker for the session. The event began with the presentation that aimed to explore the dynamic relationship between advancements in electronics and their applications in space exploration, as well as the reciprocal influence of space missions on electronic technology. The speaker emphasized the development of radiation-hardened electronics to withstand space's extreme conditions, such as

Co-Convener

Dr. Mathan N,

Asst. Prof., Dept. of ECE

Advisor, IEEE SBC - CAS



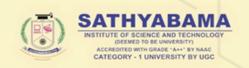
The guest speaker outlined how electronics are fundamental to space missions, from satellite communication systems to spacecraft control mechanisms. Modern electronics enable precise navigation, data collection, and real-time communication with Earth. Emphasis was placed on the development of radiation-hardened electronics designed to withstand the harsh conditions of space, such as high levels of radiation and extreme temperatures. The talk covered recent advancements in space science that have been made possible due to innovations in electronics. For instance, improved sensors and imaging technologies have enhanced our ability to study distant celestial bodies and gather detailed data from space missions.

The guest highlighted specific missions where cutting-edge electronic systems played a crucial role, such as the Mars rovers and space telescopes. The speaker introduced several emerging technologies that are set to transform space exploration, including miniaturized electronics for CubeSats and advanced data processing systems for deep space exploration. Discussions also focused on the potential future synergies between electronics and space science, such as the integration of artificial intelligence for autonomous spacecraft operations and advanced communication systems for interstellar exploration

The presentation concluded with an exploration of how advancements in space electronics have broader implications for other fields, such as telecommunications, medical technology, and consumer electronics. The speaker illustrated how technologies developed for space missions often find applications in everyday life.

23-08-2024

"SPACE ODYSSEY - Signal Showdown **Brain Teaser**"







IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COMMEMORATING NATIONAL SPACE DAY 2024

SPACE ODYSSEY

"Signal Showdown Brain Teaser"







Chief Patrons Dr. Mariazeena Johnson, Chancellor

Dr. Marie Johnson, President

Mr. J Arul Selvan, Vice President

Ms. Maria Bernadette Tamilarasi,

Vice President

Ms. Maria Catherine Johnson,

Vice President





Dr. E Logashanmugam, Pro Vice Chancellor

Dr. S S Rao, Registrar Dr. G Sundari, Director-Administration Dr. N M Nandhitha, Dean School of EE

Dr. T Ravi, Head, Dept. of ECE



REGISTER HERE https://bit.ly/space_odyssey_ieee

Convener Dr. M S Godwin Premi, Prof., Dept. of CSE IEEE SB Counsellor Co-Convener Dr. Mathan N.

Asst. Prof., Dept. of ECE Advisor, IEEE SBC - SPS

> **Student Organizers** Ms. Darrshini V B, Vice Chair, IEEE SB

Ms. Sherin S, Vice Chair, IEEE SBC - SPS

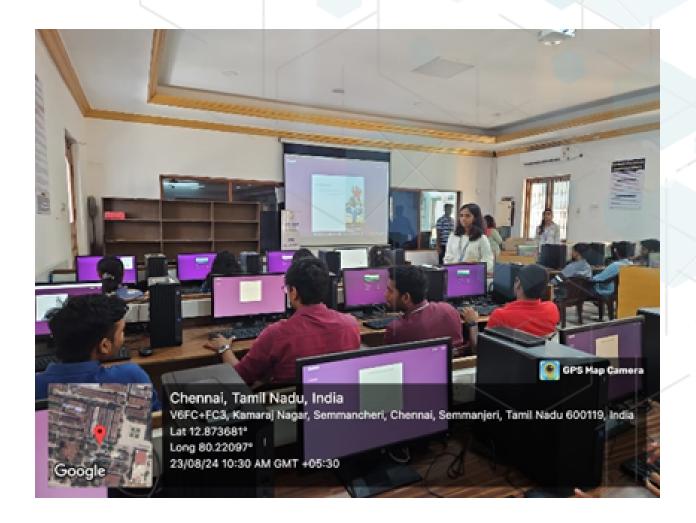
Ms. Radhika.T,

Member, IEEE SBC - SPS Mr.Jaiganesh.V, Member, IEEE SBC - SPS

IEEE Student Branch Chapter IEEE Signal Processing Society organizes a quiz competition Commemorating National Space Day 2024 "SPACE ODYSSEY" -Signal Showdown Brain Teaser on 23rd August 2024 at VLSI Design Lab,

ECE Second Floor in Sathyabama Institute of Science and Technology, Chennai. The competition consisted of four rounds namely Startburst Start, Pulse or Ploy , Cosmic Countdown and Blackhole challenge.

Round 1 Startburst Start Kick off the guiz with a burst of energy and set the stage for an engaging competition. Description: Participants will face a series of fast-paced, multiple-choice questions designed to warm up their brains and get them in the competitive spirit. The questions will cover a broad range of topics, including current events, history, and popular culture. This round is intended to energize participants and gauge their initial performance levels.



Round 2 Pulse or Ploy energies to test participants' ability to think on their feet and differentiate between factual information and deceptive or misleading statements. Description: In this round, participants will encounter questions that present a mix of true and false information. They must determine whether each statement is accurate ("Pulse") or a trick ("Ploy"). This round will challenge their critical thinking skills and attention to detail.

Round 3 Cosmic Countdown measures participants' knowledge on a variety of subjects under time pressure. Description: Participants will answer questions related to science, astronomy, and space exploration. Each question will have a countdown timer, adding an element of urgency to the task. This round aims to evaluate their knowledge depth and speed in a high-stakes environment. Round 4 Black Hole Challenge concludes the quiz with a high-difficulty segment that tests participants' overall knowledge and problem-solving abilities.

This final round consists of complex questions and puzzles that cover diverse topics. The difficulty level will be significantly higher, requiring participants to use their accumulated knowledge and logical reasoning skills. The Black Hole Challenge is designed to identify the ultimate quiz champion.



The event promises to be an intellectually stimulating and entertaining experience. Each round is crafted to challenge different aspects of participants' knowledge and skills, culminating in a thrilling finale. We anticipate an engaging and competitive event that will test the mettle of all participants.

23-08-2024

THE COSMIC CAST "LET'S TALK BEYOND EARTH"





On 23rd August 2024, "The Cosmic Cast: Let's Talk Beyond Earth" captivated attendees at the ECE Seminar Hall. Organized by the Sathyabama IEEE Student Branch and the Electronics and Communication Engineering Division, the event drew 30 corporate participants alongside students from diverse departments. Participants displayed great enthusiasm, engaging in thought-provoking discussions on space exploration and its technological challenges. The interactive Q&A sessions saw students actively posing innovative questions and proposing creative solutions, demonstrating a genuine curiosity about the cosmos. The event fostered a collaborative atmosphere, encouraging attendees to think critically about futuristic advancements beyond Earth.

TECHNO FIT-CHECK

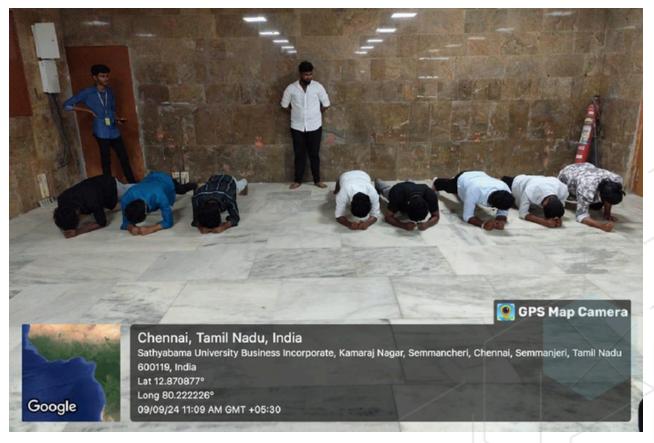


Techno Fit-Check was an engaging and fun-filled event, designed to challenge both the minds and endurance of the participants. The competition was structured into two exciting rounds. The first round was a Crossword Puzzle focused on sports, testing the participants' knowledge of various sports terminology, famous athletes, and sporting events.

The second round saw participants facing the Plank Challenge, where they were required to maintain a plank position for as long as possible, testing their physical strength, endurance, and mental toughness.

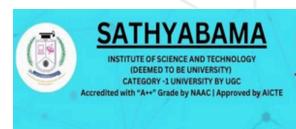


With a total of 57 attendees, the event was a thrilling blend of intellectual stimulation and physical challenge, encouraging teamwork, strategy, and resilience. The event was organized by the Department of Computer Science and Engineering and was a resounding success, providing participants with an opportunity to showcase their skills in both cognitive and physical domains.



10-09-2024

IEEE Awareness session on IEEE Power Electronics Society





IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING



Ms.Krishnaa Jayan

IEEE Member

ORGANIZES AWARENESS SESSION ON

ieee Power Electronics

Society



Mr.Rajesh G, Chair IEEE/PELS



Time: 10.00AM - 11.15AM



Date: 10 Sept 2024

CHIEF PATRONS:

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Vice President

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Dr. G. Sundari, Director Administration

Dr. N.M.Nandhitha, Dean/SOEE

Dr. S.Radhika, Head, Dept. of EEE

CONVENOR:

Dr. M. S. Godwin Premi,

Prof. Dept of CSE.

IEEE SB Counsellor

Co-CONVENORS:

Dr. M.L.Bharathi,

Asso.Prof, Dept. of EEE

Advisor, IEEE SBC-PELS

STUDENT COORDINATORS:

Mr.Rajesh G,

Chair, IEEE PELS

Ms.Aditi Bhowmik,

Vice-chair, IEEE PELS

Ms. Mahsar,

Secretary, IEEE PELS

On 10th September 2024, the IEEE Power Electronics Society (PELS) conducted an awareness program to introduce 1st-year students to the field of power electronics and the benefits of joining the IEEE PELS community.

The event began with a warm welcome from Mukilabama, a 3rd year IEEE PELS member. Mukila Bama shared personal experiences about the opportunities and benefits of being part of IEEE PELS and encouraged the 1st-year students to take advantage of this platform to enhance their technical skills and professional network.



The program continued with a brief introduction to IEEE PELS, explained by Krishnaa Jayan,3rd year IEEE Member about the global impact on power

electronics and its role in advancing technologies like electric vehicles, renewable energy, and energy-efficient systems. The benefits of joining IEEE PELS were discussed, including access to technical resources, networking opportunities, technical competitions, and workshops that can help students develop both technical and professional skills. Mr.G. Rajesh, the Chair IEEE PELS, gave a talk emphasizing the importance of joining IEEE as a whole, beyond just PELS.





He explained the many benefits of IEEE membership, including access to a vast library of technical resources, global conferences, and networking opportunities with industry professionals and leading academics. He stressed that IEEE is not just for engineers interested in power electronics, but also for those pursuing interests in various engineering fields. He explained how IEEE provides platforms for leadership, competitions, and global recognition, making it an essential tool for career development. IEEE SBC Advisor Dr. M. L. Bharathi ma'am, delivered an inspiring talk aimed at motivating the new students to join the IEEE PELS Society. She emphasized the importance of stepping into professional societies early in their academic journey. Bharathi Ma'am explained how IEEE PELS provides a platform to explore new technologies, gain industry insights, and develop leadership and communication skills. She highlighted that being part of IEEE PELS would help students grow academically while also opening doors to internships, research opportunities, and career advancements. She encouraged all 1st-year students to get involved early and make the most of the resources and networks the society offers. Aditi Bhowmik, a 3rd year IEEE Member delivered the vote of thanks, expressing appreciation to Bharathi Ma'am, the organizers, and the attendees. She encouraged the 1st-year students to join IEEE PELS and emphasized the benefits of being part of a global community focused on power electronics. The IEEE PELS Awareness Program was a successful initiative to introduce 1st- year students to the exciting opportunities within the field of power

electronics. With talks from experienced students and faculty like Bharathi Ma'am, the event motivated the new students to join IEEE PELS and embark on their journey of professional development.

12-09-2024

CONCATENATION (CONNECTIONS - KNOWLEDGE KNOCKOUT)







IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

ORGANIZES



CONCATENATION

CONNECTIONS - KNOWLEDGE KNOCKOUT

DATE: 12/09/2024

TIME: 1.15 PM TO 3.15 PM

VENUE: MEGA CLASSROOM

E-CERTIFICATES WILL BE ISSUED



https://forms.gle/Ftynz UKJUibF2CDQ8

> CONVENOR Dr. M S Godwin Premi

Prof., Dept. of CSE IEEE SB Counsellor

CO - CONVENOR
Dr . R Narmada
HOD/MECHATRONICS

IEEE SBC - APS

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Vice-chair IEEE SBC-APS

Ms Shabasri S Secretary IEEE SBC-APS

Ms Anjalaatche LB
Treasurer IEEE SBC-APS

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Director-Administration

Dr. N M Nandhitha

Dean School of EE



On 23rd August 2024, "The Cosmic Cast: Let's Talk Beyond Earth" took participants on an exciting journey through the realms of space exploration. Hosted at the ECE Seminar Hall, the event, organized by the Sathyabama IEEE Student Branch in collaboration with the Electronics and Communication Engineering Division, attracted a diverse audience, with 30 corporate participants and 40 students from various departments.

The event sparked great enthusiasm, with participants eagerly engaging in dynamic, thought-provoking discussions on the latest advancements in space technology and the challenges of exploring beyond Earth. The energy in the room was palpable, as both corporate professionals and students shared ideas and insights about the future of space travel, satellite technology, and interplanetary exploration.

One of the highlights was the interactive Q&A session, where attendees, fueled by genuine curiosity, posed creative questions and offered innovative solutions to the challenges of space exploration. The discussions demonstrated the attendees' eagerness to learn, collaborate, and think critically about the mysteries of the cosmos.

Overall, the event successfully created a collaborative atmosphere, encouraging participants to broaden their horizons and delve into futuristic technological concepts. It was an inspiring and exhilarating experience for everyone involved, leaving all attendees excited about the endless possibilities beyond Earth.

GEO QUEST



The event is designed to challenge participants with a mix of online and live quiz rounds, encouraging fast responses, strategic thinking, and teamwork. Students compete across multiple formats, from individual time-based quizzes to strategic team-based rounds, making the competition exciting and dynamic.

The event is designed to challenge participants with a mix of online and live quiz rounds, encouraging fast responses, strategic thinking, and teamwork.

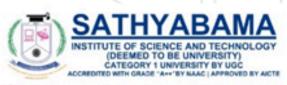




Students compete across multiple formats, from individual time-based quizzes to strategic team-based rounds, making the competition exciting and dynamic. The fast-paced nature of the event pushes participants to think on their feet, while the strategic elements keep them engaged throughout. It's a thrilling competition that tests both knowledge and quick decision-making under pressure. The event fosters a sense of collaboration and competition, motivating students to work together while striving for individual excellence.

The objectives of the event include fostering teamwork and collaboration in a competitive environment, testing participants' knowledge across various fields such as current events, satellite imagery, and trivia, and encouraging quick decision-making and strategic thinking with time constraints and point-based challenges. The first stage, the online preliminary round, involves an online quiz with multiple-choice questions, where students answer as quickly as possible to earn more points. The simplicity of online participation, combined with time-based scoring, creates an exciting and competitive atmosphere. The second stage, the Final Jeopardy round, is a more strategic quiz where teams select questions from various categories with different point values. Teams risk points with each answer, and they can bet points on a high-stakes question for a chance to boost their score. This round brings a sense of unpredictability, as teams must weigh the risks and rewards, keeping the competition thrilling until the very end.

IEEE Day Celebration



























IEEE STUDENT BRANCH

Celebrating





Join & Experience!



Dare DevCon

Code Carnival

Enviro Lens

Techno Tales

Code Rush

Charge-up Conclave **Photonics Quiz**

Tech Charades

Celestial Canvas

ElectroQuest

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01-10-2024

10 AM

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Dr.A.Mary Posonia, Head/CSE Dr.S.Revathy, Head/IT Dr.T.Ravi, Head/ECE Dr.S.Radhika, Head/EEE

Dr. M. S. Godwin Premi, Prof. /CSE IEEE SB Counsellor

CONVENOR

Dr. Joshila Grace L. K., Prof. / CSE Dr. S Prayla Shyry , Prof / CSE Advisor, IEEE SBC - ITS, CIS & PS Advisor, IEEE SBC - CS, RAS & CRSS

Guest of Honour

Mr Abdul Halik M I

Vice Chair of Branding, IEEE Day 2024 Secretary, IEEE Madras Young Professionals, Operations Lead, Forte Consulting



Presentation Hall, Central Library

STUDENT COORDINATORS

Ms. Dhivya G, Chair, IEEE SB Ms. Darrshini.V.B , Vice Chair, IEEE SB Mr. Abishek DG , Secretary, IEEE SB Ms. Bhavya B V , Treasurer, IEEE SB

Mr. Akshaya Kumar N , IEEE Webmaster, IEEE SB

CO-CONVENORS

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Ms. Shamini .C.I

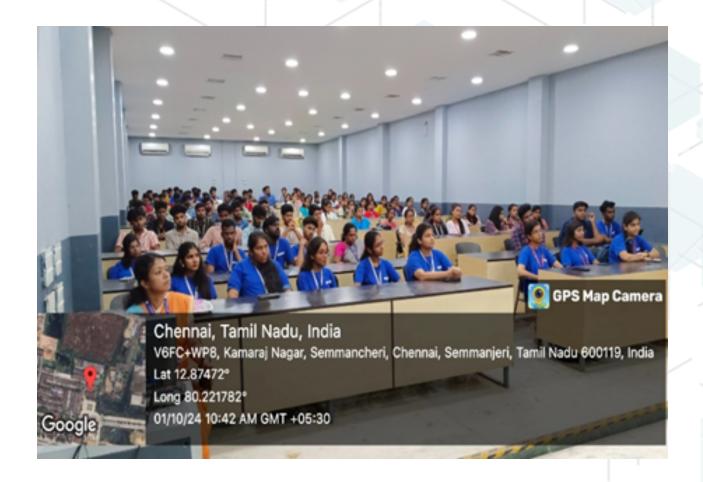
Dr. M.L. Bharathi , Assoc. Prof. / EEE Dr. R Narmadha ,

H00 / Mechatronics Advisor, IEEE SBC - APS



The IEEE Student Branch of Sathyabama Institute of Science and Technology celebrated IEEE Day with great enthusiasm, commemorating the first gathering of engineers to share technical ideas in 1884. The event brought together students, faculty, and IEEE members to promote innovation and collaboration. A series of engaging technical and creative activities were organized, reflecting the theme, "Leveraging Technology for a Better Tomorrow." The day aimed to inspire students to embrace cutting-edge technologies and actively contribute to the global engineering community.

The event was inaugurated by Mr. Abdul Halik M.I., Vice Chair of Branding for IEEE Day 2024 and Secretary, IEEE Madras Young Professionals. In his address, he highlighted the significance of IEEE in fostering technological advancements and interdisciplinary research. He encouraged students to actively participate in IEEE initiatives and leverage networking opportunities for professional growth. He also shared insights on how young engineers can drive innovation and build sustainable solutions to address global challenges.



Event Descriptions:-

Dare DevCon

A platform for aspiring developers to showcase their skills, Dare DevCon challenged participants to create innovative solutions for real-world problems. Teams collaborated to present their ideas within a tight timeframe, fostering creativity and teamwork. The event sparked a competitive spirit and encouraged the exploration of emerging technologies. The winners impressed judges with their practical and impactful prototypes.

<u>Icon Design</u>

Icon Design brought out the creative flair of participants, where they crafted digital icons representing technological concepts. Students showcased their graphic design skills and innovative thinking. The event emphasized the importance of visual communication in the tech world. Unique and visually appealing designs were recognized for their originality and relevance.

Code Carnival

Code Carnival was a coding competition that tested participants' problem-solving and programming abilities. It featured challenging algorithmic problems requiring logical thinking and speed. Coders from various disciplines enthusiastically participated, making the event a success. The competition provided a platform for students to enhance their coding expertise and earn recognition.

Charge-up Conclave

This panel discussion brought experts and students together to discuss emerging trends in technology and engineering. Topics included sustainable energy, Al innovations, and industry expectations. The conclave offered valuable insights into future opportunities in the tech sector. It also inspired students to pursue knowledge beyond the classroom.

Photonics Quiz

The Photonics Quiz tested participants' knowledge in optics, lasers, and photonics technologies. It covered basic concepts and advanced applications in various fields, from telecommunications to healthcare. The event stimulated interest in photonics research and innovation. Participants enjoyed the intellectually stimulating environment and learned new facts.

Enviro Lens

Enviro Lens focused on creating innovative solutions for environmental challenges using technology. Participants were tasked with proposing sustainable solutions for issues like pollution, waste management, and renewable energy. The event fostered awareness about environmental issues and highlighted the role of technology in building a greener future.

Tech Charades

Tech Charades was a fun-filled event that combined technology and entertainment. Participants acted out technical terms, gadgets, or concepts while their teammates guessed the answers. The event encouraged quick thinking and teamwork in a lighthearted atmosphere. It was a crowd favorite, with students enjoying the mix of fun and learning.

Techno Tales

In Techno Tales, participants narrated stories linking technology with real-world applications. They showcased their storytelling and presentation skills, connecting innovations to everyday challenges. The event demonstrated how storytelling can effectively communicate complex ideas. The best stories were both engaging and thought-provoking.

Celestial Canvas

This creative event allowed participants to visualize and design futuristic space technologies. It combined art and engineering, inspiring students to imagine space exploration innovations. Participants presented unique ideas, ranging from space habitats to interplanetary transportation systems. The event highlighted the role of imagination in scientific advancements.

Code Rush

Code Rush was an intense, time-bound hackathon where participants competed to solve coding challenges. The event tested speed, accuracy, and problem-solving under pressure. It provided an excellent opportunity for students to sharpen their technical skills. The top performers displayed exceptional coding efficiency and logical reasoning.



ElectroQuest

ElectroQuest was an exciting technical quiz focused on electronics and circuits. Participants answered questions ranging from fundamental concepts to advanced circuit design. The event aimed to promote a deeper understanding of electronics and its applications. Teams demonstrated their technical prowess, making the competition both engaging and educational.



04-10-2024

Guest Talk on "Emerging Trends in Front End VLSI Design"

by Mr. Sandeep Prabhakaran, a Verification Engineer at Nvidia Corporation, Santa Clara, California, USA.









IEEE STUDENT BRANCH DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING **ORGANIZES**

Guest Talk on

Emerging Trends in Front-End VLSI Design





Dr. Mariazeena Johnson, Chancellor Dr. Marie Johnson, President Mr. J Arul Selvan, Vice President

Chief Patrons

Ms. Maria Bernadette Tamilarasi, Vice President

Ms. Maria Catherine

Johnson,

Vice President Dr. M S Godwin Premi,

Patrons

Dr. T Sasipraba, Vice Chancellor

Dr. E Logashanmugam, Pro Vice Chancellor

Dr. S S Rao, Registrar

Dr. G Sundari, Director-Admin

Dr N M Nandhitha, Dean, School of EE

Dr T Ravi, Head, Dept. of ECE

Convener

Prof., Dept. of CSE

Mr. Sandeep Prabhakaran Verification Engineer,

Nvidia Corporation, Santa Clara, California, USA

Ms. Bhavya B V,

Student Organizers

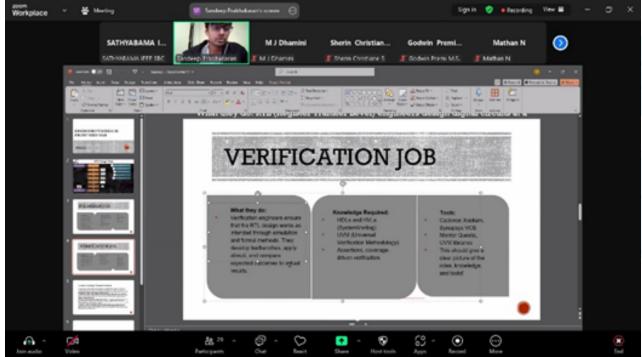
Co-Convener Dr. Mathan N, Asst. Prof., Dept. of ECE

Treasurer, IEEE SBC Ms. Dhamini M J. Webmaster, IEEE SBC - CAS Advisor, IEEE SBC - SPS & CAS

IEEE SB Counsellor Sathyabama IEEE Student Branch Chapter Signal Processing Society and Circuits and Systems Society had the privilege of hosting an insightful guest talk on "Emerging Trends in Front-End VLSI Design" by Mr. Sandeep Prabhakaran, a Verification Engineer at Nvidia Corporation, Santa Clara, California, USA. The talk provided valuable insights into the latest advancements and challenges in the field of front-end VLSI design.

Mr. Prabhakaran began his talk by highlighting the increasing complexity of modern integrated circuits (ICs), driven by the relentless demand for higher performance, lower power consumption, and smaller form factors. He emphasized the critical role of front-end design in ensuring the overall functionality and reliability of ICs.





The speaker discussed several emerging trends in front-end VLSI design, including:FinFET Technology: Mr. Prabhakaran explained the advantages of FinFET transistors in terms of their improved performance and power efficiency compared to traditional planar transistors. He also discussed the challenges associated with FinFET design, such as leakage current and variability.

· 3D-ICs: The speaker explored the concept of 3D-ICs, which stack multiple layers of circuitry vertically to achieve higher density and performance. He highlighted the potential benefits and challenges of 3D-IC technology, including thermal management and inter-layer communication.

Al and Machine Learning: Mr. Prabhakaran discussed the growing application of Al and machine learning techniques in front-end VLSI design. He explained how these technologies can be used to automate design tasks, optimize circuit performance, and improve fault detection.

Design for Testability (DFT): The speaker emphasized the importance of DFT in ensuring the manufacturability and reliability of ICs. He discussed various DFT techniques, such as scan chains and built-in self-test (BIST), and their role in improving fault coverage.

Mr. Prabhakaran also shared his experiences working on cutting-edge VLSI projects at Nvidia Corporation. He provided valuable insights into the design and verification process, as well as the challenges faced by front-end design engineers.

The talk concluded with a lively question-and-answer session, where Mr. Prabhakaran patiently answered questions from the audience. The students were eager to learn more about the latest trends in VLSI design and their potential applications.

The guest talk by Mr. Sandeep Prabhakaran was a great success, providing the students with a valuable opportunity to learn from an industry expert. IEEE SBC would like to thank Mr. Prabhakaran for his time and for sharing his valuable insights with the student community.

27-11-2024

Future trends and digital skills

by Ms. Sandhya K Ravi, HR & TA @ Aspire **Systems**







IEEE STUDENT BRANCH

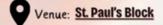
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING **ORGANIZES**

SERIES OF LECTURE ON

FUTURE TRENDS AND DIGITAL SKILLS"

(L) Time: 10:00 AM - 11:00 AM

::: Date: 27.11.2024





SANDHYA K RAVI



*E-CERTIFICATES WILL BE PROVIDED

Author & HR - Talent Acquisition UK/EU Specialist @Aspire Systems

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Chair, IEEE SBC-CIS

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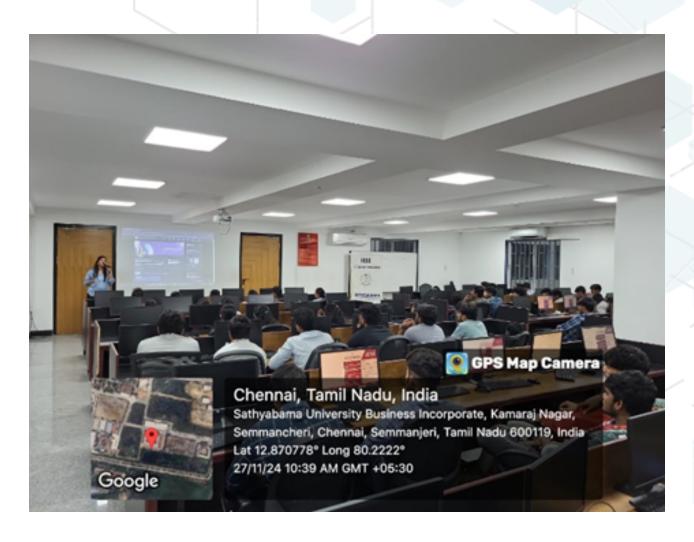
Mr. Abhinav P Menon

Seceratary, IEEE SBC-CIS

Ms. Akaluah N

Member, IEEE SBC-CIS

Ms. Sandhya K Ravi, HR & TA @ Aspire Systems, a dynamic speaker, engaged students at Sathyabama University in a compelling event organized by IEEE -Computational Intelligence Society members and Faculties. Focused on LinkedIn Branding, Email Etiquettes, Professional Etiquettes and Self Grooming. The event sparked enthusiasm, motivating students to explore the opportunities and contributions available in real time.

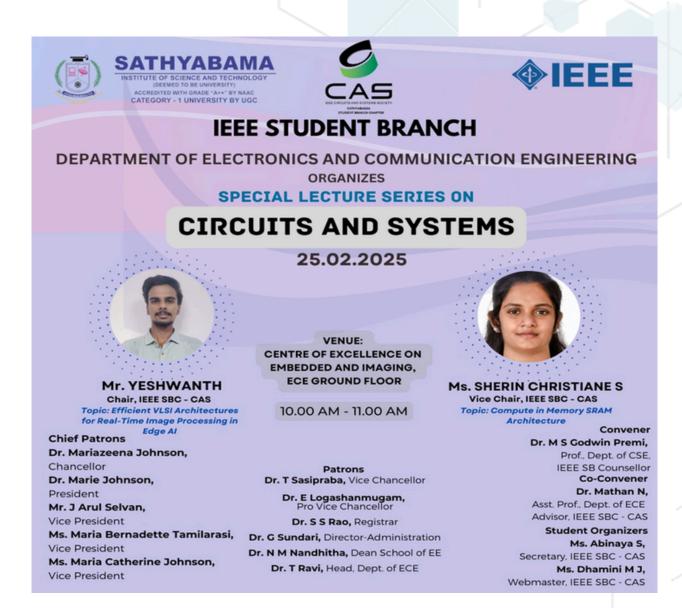




25-02-2025

Special Lecture series on Circuits and Systems

Topic 1: Efficient VLSI Architectures for Real-Time Image Processing in Edge AI by Mr. Yeshwanth, Chair, IEEE SBC- CAS Topic 2: Compute in Memory SRAM Architecture by Ms. Sherin, Vice Chair, IEEE SBC-CAS



The IEEE Student Branch of the Department of Electronics and Communication Engineering at Sathyabama Institute of Science and Technology successfully organized a Special Lecture Series on Circuits and Systems on 25th February 2025. This knowledge-sharing session was aimed at enlightening students on the latest advancements in VLSI architectures and memory computing, with a strong emphasis on Edge AI and Compute-in-Memory technologies. The event witnessed active participation from students , fostering an engaging learning environment.

The session was presided over by Mr. Yeshwanth, Chair of IEEE SBC - CAS, and Ms. Sherin Christiane S, Vice Chair of IEEE SBC - CAS. The two distinguished speakers, Mr. Yeshwanth and Ms. Sherin Christiane S, delivered insightful lectures on modern VLSI design and memory architectures, offering valuable perspectives on emerging trends in circuit design.



Mr. Yeshwanth opened the session with an insightful discussion on VLSI architectures optimized for real-time image processing, particularly in the context of Edge AI applications. He emphasized the growing demand for efficient and low-power hardware architectures that can process image data in real-time while reducing latency. Edge AI is revolutionizing image processing by enabling computations directly on edge devices instead of cloud-based processing, improving response time and security. Efficient VLSI architectures play a crucial role in enhancing parallel processing capabilities, reducing power consumption, and optimizing hardware resource utilization. The audience engaged in discussions about future advancements in AI-driven image processing and its applications in autonomous vehicles, medical imaging, and smart surveillance systems.



The second session was delivered by Ms. Sherin Christiane S, who spoke on Compute-in-Memory (CIM) SRAM Architecture, a paradigm shift in memory technology designed to overcome data transfer bottlenecks in modern computing systems.

The session concluded with an interactive discussion on the challenges of scaling CIM technology, including design complexity, memory reliability, and integration with existing computing architectures. Students gained a deeper understanding of how memory-centric processing can drive the future of computing efficiency.

05-03-2025

Riddlora-The Power of Women in Every Puzzle











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RIDDLORA

("THE POWER OF WOMEN IN EVERY PUZZLE!")

Only Girls



Register 5

COMMEMORATING INTERNATIONAL WOMEN'S DAY 2025







DATE 05 MAR 2025

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 Ms. Deepapriyaa T, Webmaster IEEE WIE AG
 Ms. Deepika K, Treasurer IEEE WIE AG

Co-Convener Dr. Mathan N

Asst. Prof., Dept. of ECE Advisor, IEEE SBC - SPS, CAS & APS

Convener
Dr. M S Godwin Premi
Prof., Dept. of CSE
IEEE SB Counsellor

The IEEE Student Branch Chapter of the Department Electronic And Communication Engineering organizes a quiz competition Commemorating International Women's Day 2025 "RIDDLORA" – The Power Of Women's In Every Puzzle on 5th March 2025 at VLSI Design Lab, ECE Second Floor in Sathyabama Institute of Science and Technology, Chennai. The event featured three intellectually stimulating rounds designed to engage participants in a competitive yet educational environment.

The competition was structured into three rounds, each focusing on different aspects of knowledge and problem-solving:

Round 1: Quiz - A multiple-choice questionnaire covering famous women and notable female achievers across various domains, testing participants' awareness and historical knowledge.Round 2: Connection Round - Another MCQ-based round featuring images related to prominent women and their contributions. Participants were challenged to identify the correct connection, enhancing their ability to analyze and correlate information. Round 3: Word Block the final and most challenging round, where word block, clues were projected, and participants had to identify words related to women's achievements, empowerment, and leadership.



Participants gained insightful knowledge about inspirational women and their contributions. The event saw enthusiastic participation, making the learning experience interactive and enjoyable. The event successfully combined education, engagement, and competition to honor the contributions of women globally. The enthusiasm and active participation of attendees contributed to making this event a grand success.

05-03-2025

Guest Talk on VLSI and Embedded **Systrems**











IEEE STUDENT BRANCH

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING **ORGANIZES**

Guest Talk on

VLSI and Embedded Systems



DATE 05 MAR 2025



by IEEE Virtual Bureau Speaker

TIME 7.00 PM TO 8.00 PM

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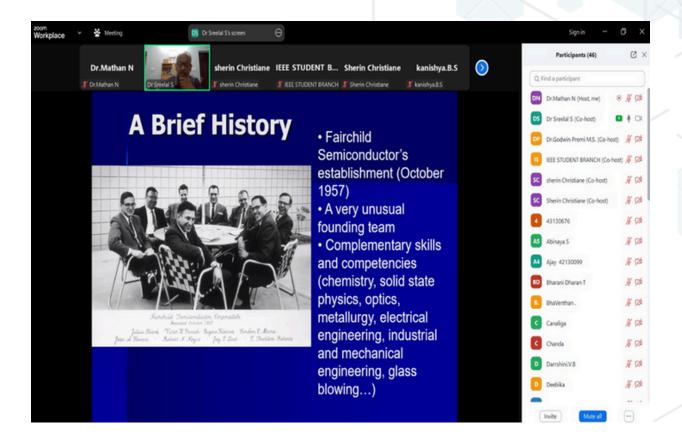
Dr. Sreelal Pillai Senior Avionics Engineer, Indian Space Research Organisation (ISRO), Chapter Chair, IEEE Aerospace & Electronic Systems Society, Kerala Chapter

Student Organizers

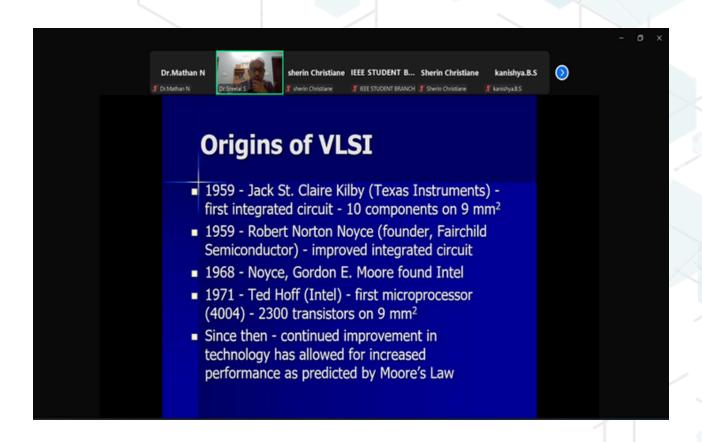
Ms. Sherin Christiane, Vice Chair, IEEE SBC - SPS

The IEEE Student Branch Chapter of the Department of Electronics and Communication Engineering at Sathyabama Institute of Science and Technology organized a guest talk on "VLSI and Embedded Systems" on 5th March 2025. This online session, hosted via the IEEE Virtual Speakers Bureau, featured Dr. Sreelal Pillai, a Senior Avionics Engineer at ISRO and Chapter Chair of the IEEE Aerospace & Electronic Systems Society, Kerala Chapter, as the keynote speaker. The event provided students with valuable insights into the advancements and challenges in VLSI and embedded systems, particularly in the context of modern technological demands.

Dr. Pillai elaborated on critical topics such as circuit design complexities, memory reliability, and integration challenges in modern computing architectures. He also discussed the evolution of silicon technology, as depicted in one of the slides presented during the session, comparing the early transistor developments at Bell Labs in 1947 to Intel's advancements in 2002, showcasing the drastic reduction in transistor size to the nanometer scale. This transformation has significantly impacted computational efficiency, paving the way for more compact and powerful electronic systems. The session was well-received, sparking engaging discussions among students and faculty, and reinforcing the importance of VLSI and embedded systems in shaping future innovations.



Dr. Sreelal Pillai started with the history of Fairchild Semiconductor's and then shared real-world case studies from his experience at ISRO, illustrating how advanced semiconductor technologies play a critical role in avionics and space exploration. He also shed light on how embedded systems are integral to mission-critical applications, requiring high reliability, efficiency, and optimization. The session highlighted the role of AI and machine learning in modern chip design, showcasing how intelligent algorithms are now used to optimize performance and power consumption in embedded devices.



The session concluded with an engaging Q&A segment, where students asked insightful questions about fabrication technologies, power management techniques, and the future of Moore's Law. The enthusiastic participation from students and faculty members made the event highly interactive and impactful. Overall, the lecture provided an invaluable learning experience, inspiring attendees to delve deeper into the evolving landscape of VLSI and embedded systems.

06-03-2025

Guest Talk on Entrepreneur as a Women









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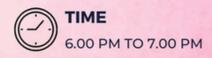
GUEST TALK ON

ENTREPRENEUR AS A WOMEN

COMMEMORATING INTERNATIONAL WOMEN'S DAY 2025







Ms. Vishalini Chandrasekar

Founder, Visara Naturals Marketing Head, Bytesor Director, Her Startup Stories

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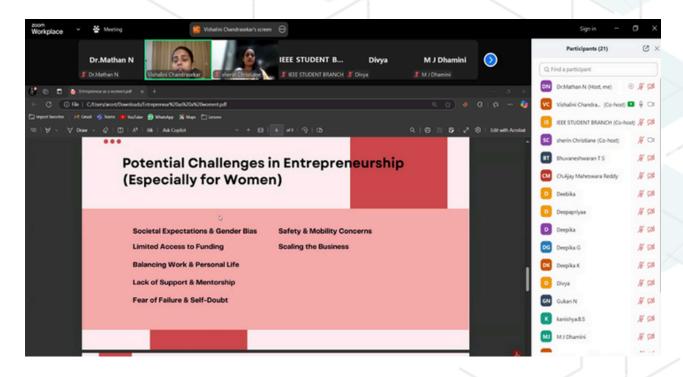
Co-Convener Dr. Mathan N

Asst. Prof., Dept. of ECE Advisor, IEEE SBC - APS

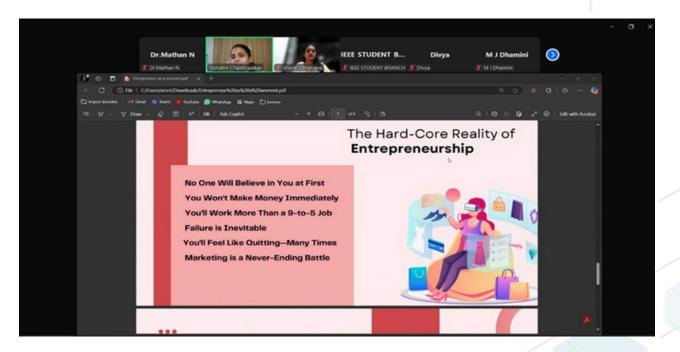
Convener
Dr. M S Godwin Premi
Prof., Dept. of CSE
IEEE SB Counsellor

The IEEE Student Branch Chapter of the Department of Electronics and Communication Engineering, Sathyabama Institute of Science and Technology, successfully organized a guest talk on "Entrepreneur as a Woman" on 6th March 2025, in commemoration of International Women's Day 2025. The event aimed to inspire and empower young minds by highlighting the entrepreneurial journey of women in business and technology.

The session was graced by Ms.Vishalini Chandrasekar, Founder of Visara Naturals, Marketing Head at Bytesor, and Director of Her Startup Stories. As a distinguished entrepreneur, she shared her experiences, challenges, and key insights into the evolving landscape of women-led businesses. She emphasized the importance of resilience, innovation, and leadership in navigating the entrepreneurial ecosystem. The discussion also focused on overcoming gender barriers, securing funding, and leveraging technology for business growth.



Ms. Vishalini addressed societal expectations, gender bias, limited access to funding, work-life balance, lack of mentorship, safety concerns, and business scalability. The interactive presentation encouraged students to actively engage with the speaker and gain a comprehensive understanding of real-world entrepreneurial hurdles.



The session on "The Hard-Core Reality of Entrepreneurship" by Ms. Vishalini offered a candid insight into the real challenges of starting a business, including lack of support, financial struggles, and self-doubt. Her inspiring talk helped students set practical expectations and prepare for entrepreneurial journeys. The event saw enthusiastic participation from faculty, students, and IEEE volunteers. An interactive Q&A session allowed students to seek guidance on startup growth and networking. The session concluded with a vote of thanks, marking a successful and motivating experience for all.

18-03-2025

CloudLaunch:Starting your Cloud Journey



IEEE Student Branch, Sathyabama Institute of Science and Technology organizes a hands-on training session on "CloudLaunch: Starting Your Cloud Journey" dated 18th of March 2025. This event conducted with the aim of equipping students with practical skills and knowledge in cloud computing, a field that is becoming increasingly relevant in the world of technology. Held at institute's premises from 10:00 AM to 12:00 PM, the event drew enthusiastic participation from students across multiple disciplines, eager to delve into the realm of cloud technology.



A significant feature of the session was its hands-on approach. Participants were guided through practical exercises that involved exploring cloud platforms, tools, and services. These activities allowed students to work with cloud gain infrastructure and valuable experience in configuring managing resources cloud in a environment. By actively engaging with material, attendees developed confidence in their ability to implement cloud-based solutions in real-world scenarios. This interactive component was crucial in helping participants move beyond theoretical knowledge develop practical skills.

In addition to covering technical aspects, the session shed light on emerging trends and future directions in cloud computing. Topics such as serverless architecture, hybrid clouds, and the role of artificial intelligence in cloud services were discussed, providing attendees with forward-looking а perspective. The importance of security in the cloud was emphasized, with detailed explanations of measures to protect data mitigate integrity and potential vulnerabilities. These discussions equipped students with а comprehensive understanding of both the opportunities and challenges associated with cloud technology.



Participants highly appreciated the session for its hands-on approach and practical relevance. Many of them noted that it helped them gain a deeper understanding of cloud-based solutions, boosting their confidence in implementing such technologies. The interactive nature of the session, which included Q&A segments, allowed attendees to directly engage with the speakers, further enhancing their learning experience. The event was not only a platform for skill-building but also a significant step toward motivating students to explore advanced topics in cloud computing and related fields.

By the end of the session, students were not only equipped with practical knowledge but were also inspired to consider the vast career opportunities that cloud computing offers. The training was a testament to the importance of experiential learning in preparing future professionals to meet the evolving demands of the technology industry. With its engaging format, practical exercises, and thought-provoking discussions, the event successfully kindled a passion for cloud computing among all those who attended, setting them on a path to explore and excel in this transformative field.



As the session drew to a close, it became clear that "CloudLaunch: Starting Your Cloud Journey" was not merely about acquiring technical skills it was a transformative experience that redefined what it means to embark on a journey in cloud computing. Beyond the hands-on exercises and technical demonstrations, participants discovered that the real value lay in the shift of perspective technology. towards They encouraged not just to operate within the frameworks presented but to imagine, innovate, and challenge the established confines of digital transformation.

Moreover, the session closed with a resounding call to action—a reminder that technological advancement is as much about human ingenuity as it is about technical proficiency. Participants were urged to carry forward the curiosity and adaptability nurtured during the training, to explore new horizons, and to remain engaged with the fast-paced evolution of cloud technology. The ripple effect of this mindset, as envisioned during the session, holds the promise of redefining industry standards and shaping a future where innovation is continuous and collaborative.



In essence, the concluding moments of this session left an indelible mark on every attendee. It sparked a movement from passive learning to active engagement, urging each participant to not only keep pace with technological progress but to be at the forefront of it. The legacy of "CloudLaunch: Starting Your Cloud Journey" is thus best encapsulated in the inspiration it ignited vision of a future where every individual is both a learner and a catalyst for change in the expansive realm of cloud technology

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SILICOK2K25

25-03-2025 to 26-03-2025



The Inauguration Ceremony of SILICON 2025, a National Level Technical Symposium organized by the IEEE Student Branch of Sathyabama Institute of Science and Technology, was held with great enthusiasm and grandeur. The event began with a warm welcome to the esteemed dignitaries, respected faculty members, and enthusiastic student participants. With the blessings of the Founder Chancellor Col. Dr. Jeppiaar and the constant support of the institution's leadership, the event was successfully organized.





Event Description:

DeCodeX

Put your logic and debugging skills to the test!.Participants must crack codes and fix logical errors in a challenging time frame.This event encourages teamwork, critical thinking, and clean coding practices.It's perfect for those who love puzzles and logic-based problems.Team size: 2 members.

Sky Surfers

Showcase your creativity by building gliders from scratch.Participants apply principles of aerodynamics in this hands-on event.Design, construct, and test your models for flight and stability.It's a solo event that lets your ideas truly take flight.Individual participation only.

Robowars

Gear up for a thrilling battle of bots! Teams design and bring robots to life to compete in action-packed faceoffs. This event tests your design, control, and competitive spirit. Witness creativity, engineering, and strategy collide in the arena. Team size: 3 members.



Shark Tank: Revamp

Pitch your most innovative business ideas to a panel of mock investors. This event is about convincing, planning, and entrepreneurial spirit. Your idea, execution, and presentation skills are key to winning. Perfect for aspiring entr



<u>Airacers</u>

Build and race your own drones through a dynamic course. Put your design and piloting skills to the ultimate test. Speed, control, and stability are all that matter. Aerial excitement meets technical prowess in this thrilling event. Team size: 2 members.



<u>Code-Clash</u>

A high-stakes coding contest for competitive programmers. Solve algorithmic challenges under time pressure with your partner. This event pushes your logic, speed, and coding precision. Ideal for students who love to crack tough programming problems. Team size: 2 members.

<u>ReelSpark</u>

Let your creativity spark on screen with short tech-themed reels!Craft compelling and informative short videos on trending topics.The event rewards storytelling, visual appeal, and innovation.Unleash your inner content creator and engage the tech crowd.Team size: 2 members.

IoNova

Present innovative IoT-based solutions to real-world problems. This event calls for both technical insight and problem-solving. Teams propose working models or concepts using smart technology. Focus is on creativity, impact, and feasibility. Team size: 2 members.



<u>Canvas Wars</u>

Flex your creative muscles in this digital/tech design contest.Participants are challenged to create compelling and innovative visuals.It blends technology, aesthetics, and storytelling in a unique format.Show the world your design speaks louder than words.Individual participation only.

Tech Clash

A fiery tech debate where ideas spark and logic rules. Participants argue both sides of trending technical topics. It enhances confidence, technical knowledge, and speaking skills. Judges assess logic, clarity, and communication. Team size: 2 members.



Swiftplay Showdown

A team-based gaming event where strategy and reflexes decide the win.Battle it out in popular games in an electrifying setup.Focus, coordination, and smart play are key to domination.Gamers, bring your Agame and team spirit!Team size: 3 members.

Tech-Dumb Characters

A guessing game with a tech twist!Participants act out tech words without speaking—only gestures allowed.It's fun, energetic, and tests your quick thinking and creativity.Laughter and learning guaranteed in this team event.Team size: 2 members.





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